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	U.S. PATENT DOCUMENTS											
Examiner	Cito	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where							
Initials*	Cite No.1	Number - Kind Code <sup>2</sup> (# known)	MM-DD-YYYY	Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear							
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	FOREIGN PATENT DOCUMENTS											
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>6</sup> (# <i>known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>©</sup>						
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Substitute	e for form 1449A/PTC	,		Complete if Known					
				Application Number	10/715,440				
INFC	RMATION	DIS	CLOSURE	Filing Date	November 19, 2003				
STATEMENT BY APPLICANT				First Named Inventor	Monier MAHER				
				Art Unit	2109				
	(Use as many she	ets as	necessary)	Examiner Name	Unknown				
Sheet	2	of	2	Attorney Docket Number	NVDA/AG-08-0114-US				

NON PATENT LITERATURE DOCUMENTS								
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, Journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, (i	T <sup>2</sup>					
	. A1	Bishop, et al. *SPARTA: Simulation of Physics on a Real-Time Architecture,* Proceedings of the 10 <sup>th</sup> Great Lakes Symposium on VLSI, pp. 177-182, 2000.						
	A2	Intel Corp. "Intel PCI and PCI Express," 1992-2004.						
	АЗ	Telekinesys Research Ltd. "Havok Game Dynamics SDK," 2002.						
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Examiner Signature	Date Considered	

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